L1Topo Status 11 July 2016

- **Firmware: no recent changes**
  - Test of larger readout buffers unfinished (but looks OK)
  - Version with J/PSI algorithms restored to be tested
- **Known HW problems**
  - Missing TOBs for octant 0 on the C(?) side
    - Suspicion its at the source, but will check reverting L1Topo FW
  - Occasional 1-2 bit shift in muon inputs => 40 MHz rates
  - Need to check fine timing of muon inputs (CTP pattern)
    - Also check its really happening at the L1Topo end
  - Also retry Muctpi2topo patch adding GTX reset
- **Other problems**
  - Eta/phi shift (0.1?) between muon and calo TOBs
    - Need to check VHDL mapping of all TOB types