



L1Topo Status 7 July 2016

- **Firmware: no recent changes**
 - Test of larger readout buffers unfinished (but looks OK)
 - Version with J/PSI algorithms restored to be tested
- **Known HW problems**
 - Missing TOBs for octant 0 on the A side
 - Occasional 1-2 bit shift in muon inputs => 40 MHz rates
 - Need to check fine timing of muon inputs (CTP pattern)
 - Also check its really happening at the L1Topo end
 - Also retry Muctpi2topo patch adding GTX reset
- **Other problems**
 - Possible eta/phi shift between muon and calo TOBs